

U.S. Department of Commerce, Patent and Trademark			Atty. Docket No.			Application No.		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT			SNDK.A06US7			09/867,836		
			Applicants			Conf. No.		
(Use several sheets if necessary)			Eliyahou Harari et al.			6307		
(Form PTO-1449)			Filing Date			Art Group		
			May 30, 2001			2824		
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	2	4,890,259	12/26/1989	Simko	365	185.03		
	3	4,989,179	1/29/1991	Simko	365	185.03		
	4	5,222,046	6/22/1993	Kreifels et al.	365	185.22		
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							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
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	9	JP 1-23878	5/9/1989	Japan	G11C	11/34		X
	10	JP 1-46949	10/11/1989	Japan	G11C	11/34		X
	11	JP 2-10596	1/16/1990	Japan	G11C	16/06	Abstract	
	12	JP 3-286497	12/17/1991	Japan	G11C	16/06	Abstract	
	13	JP 4-82090	3/16/1992	Japan	G11C	16/06	Abstract	
	14	JP 9-106689	4/22/1997	Japan	G11C	16/06	Abstract	
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<i>at</i>	17	PTO, "Office Action," mailed in related Application No. 09/129,675 on July 12, 2006, 16 pages.	
<i>at</i>	18	Prior Art Cited by STMicro re U.S. Patent 5,172,338 – U. S. Patent and Application, 34 pages.	
<i>at</i>	19	Prior Art Cited by STMicro re U. S. Patent 5,172,338 – Foreign Patents/Applications; Publications; Data Sheets and Web Pages, 27 pages.	
<i>at</i>	20	Public Version of Complainant SanDisk Corporation's Reply to Respondents' Post-Hearing Brief, Investigation No. 337-TA-526, October 7, 2005, pp. 81-128.	
<i>at</i>	21	Public Version of Complainant SanDisk Corporation's Post-Hearing Brief, Investigation No. 337-TA-526, October 7, 2005, pp. 99-134.	
Examiner <i>Al</i>		Date Considered	<i>12/19/2006</i>
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>			

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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

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							Translation
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at	4	"An Analysis of SGS Thomson M293B1 EPM32: Electronic Program Memory for 32 Stations", <u>Chipworks</u> , October 19, 1996.
at	5.	"SGS M293 Electronic Program Memory Device", <u>Taeus Report 60723</u> , August 9, 1996.
at	6.	"Initial Determination, U.S. International Trade Commission, Investigation No. 337-TA-382, March 25, 1997.
at	7.	Japanese Patent Office Appeal/Trial Decision in Japanese Patent Application No. 2-506478, October 23, 2001.

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at	6	David A. Rich, "A Survey of Multivalued Memories," IEEE Transaction of Computers, Vol. C-35, No. 2, February, 1986, pp. 99-106.					
at	7	Clinton Kuo et al., "An 80 ns 32K EEPROM Using the FETMOS Cell," IEEE Journal of Solid-State Circuits, Vol. SC-17, No. 5, October, 1982, pp. 821-827.					
at	8	Electronic Industries Association, JEDEC Standard, "Configurations for Solid State Memories, JEDEC Standard No. 21-B (Revision of JESD21-A and JESD21-A-1), CS-0336, 337-TA-560, December, 1988, pp. i-36, 39-147.					
at	9	"Expert Report of Dr. Richard Pashley Regarding the Invalidity of U.S. Patent No. 5,172,338 and U.S. Patent No. 5,991,517," United States International Trade Commission, Investigation No. 337-TA-560, September 7, 2006, pp. i-387.					
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at	11	"Addendum to the Rebuttal Expert Report of G.R. Mohan Rao, Ph.D. Regarding Validity and Enforceability of U.S. Patent Nos. 5,172,338 and 5,991,517," United States International Trade Commission, Investigation No. 337-TA-560, September 28, 2006, 5 pages.					
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